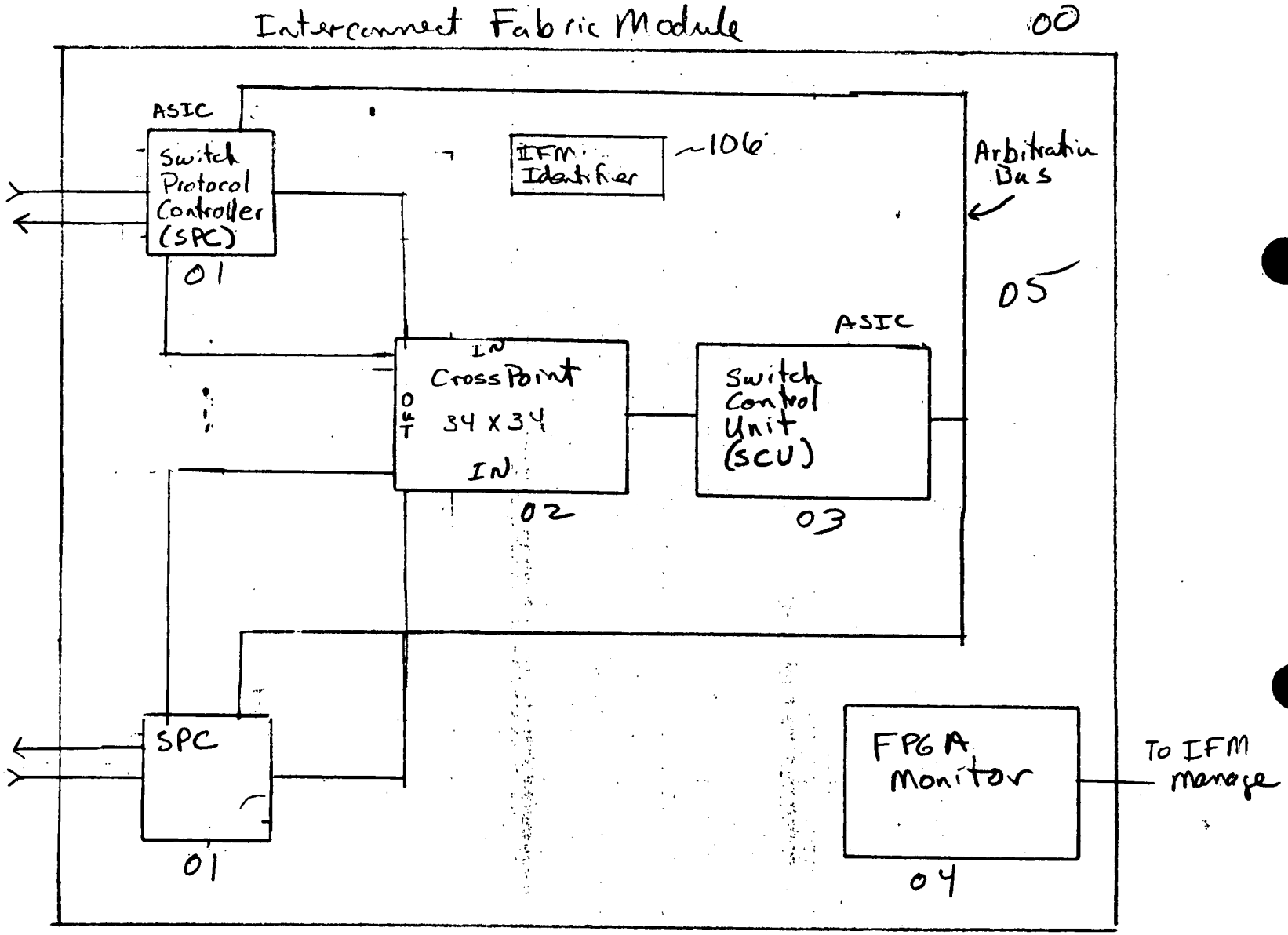

Interconnect Fabric Module
00
ASIC
Switch Protocol Controller (SPC)
01
IFM Identifier
106
Arbitration Bus
05
ASIC
IN
CrossPoint
OUT 34 X 34
IN
02
Switch Control Unit (SCU)
03
SPC
01
FPGA Monitor
04
To IFM manage

Fig 1

T09207"2594007

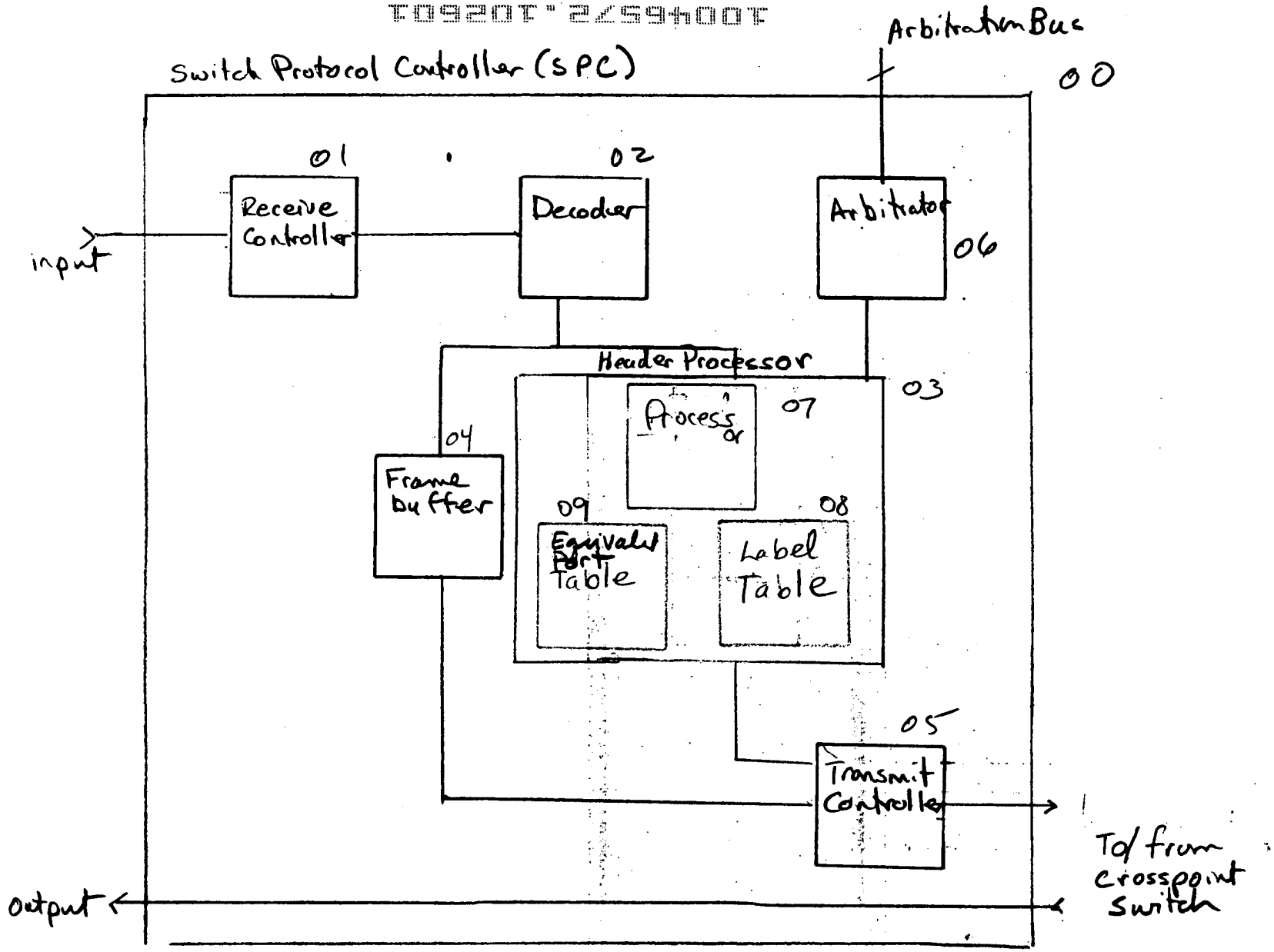


Fig 2

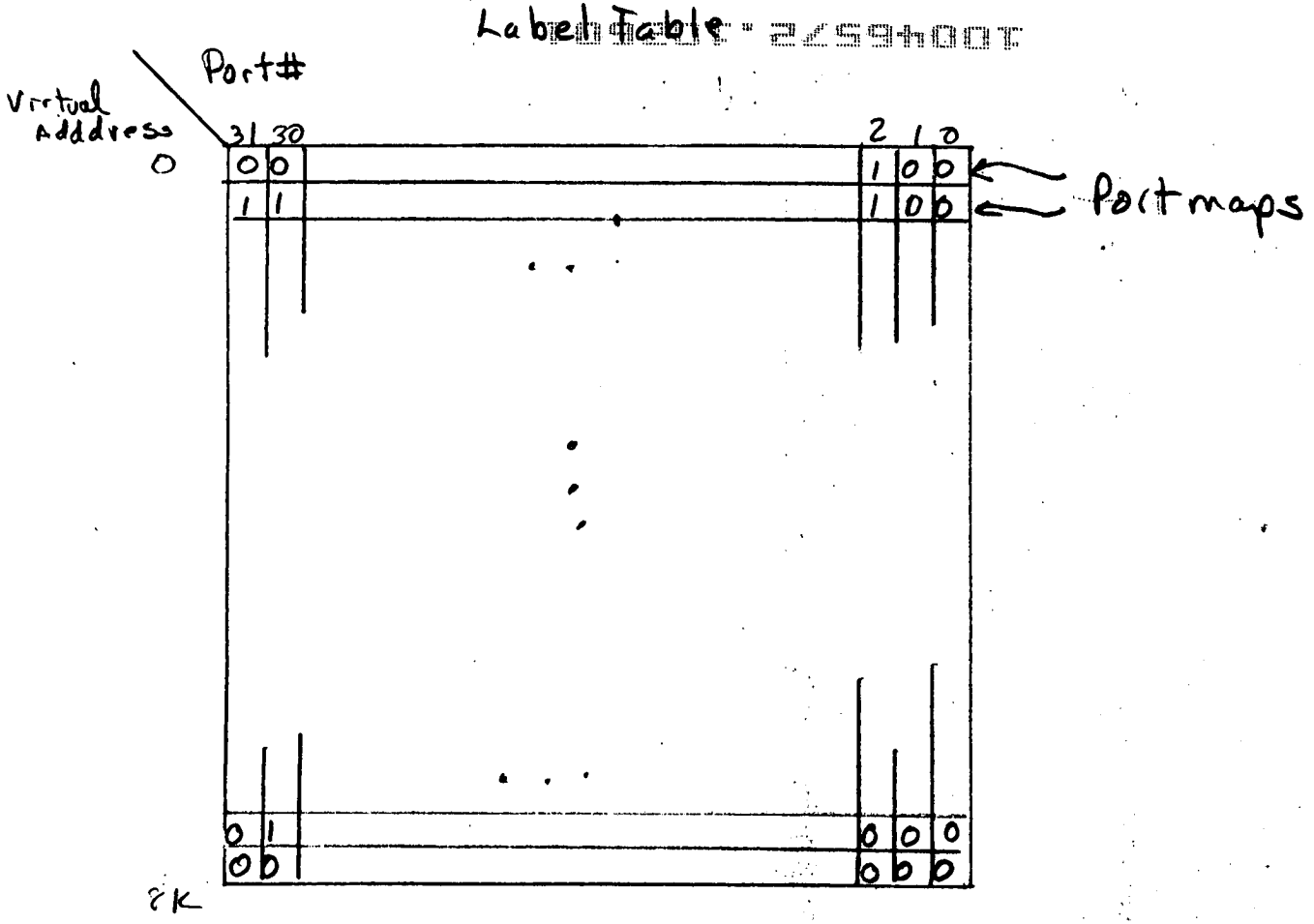

Label Table - 259th001
Port#
Virtual Address
31 30
2 1 0
0
1
0 0
1 1
1 0 0
1 0 0
Port maps
0 1
0 0
0 0 0
0 0 0
8K

Fig 3

T09207'2594007

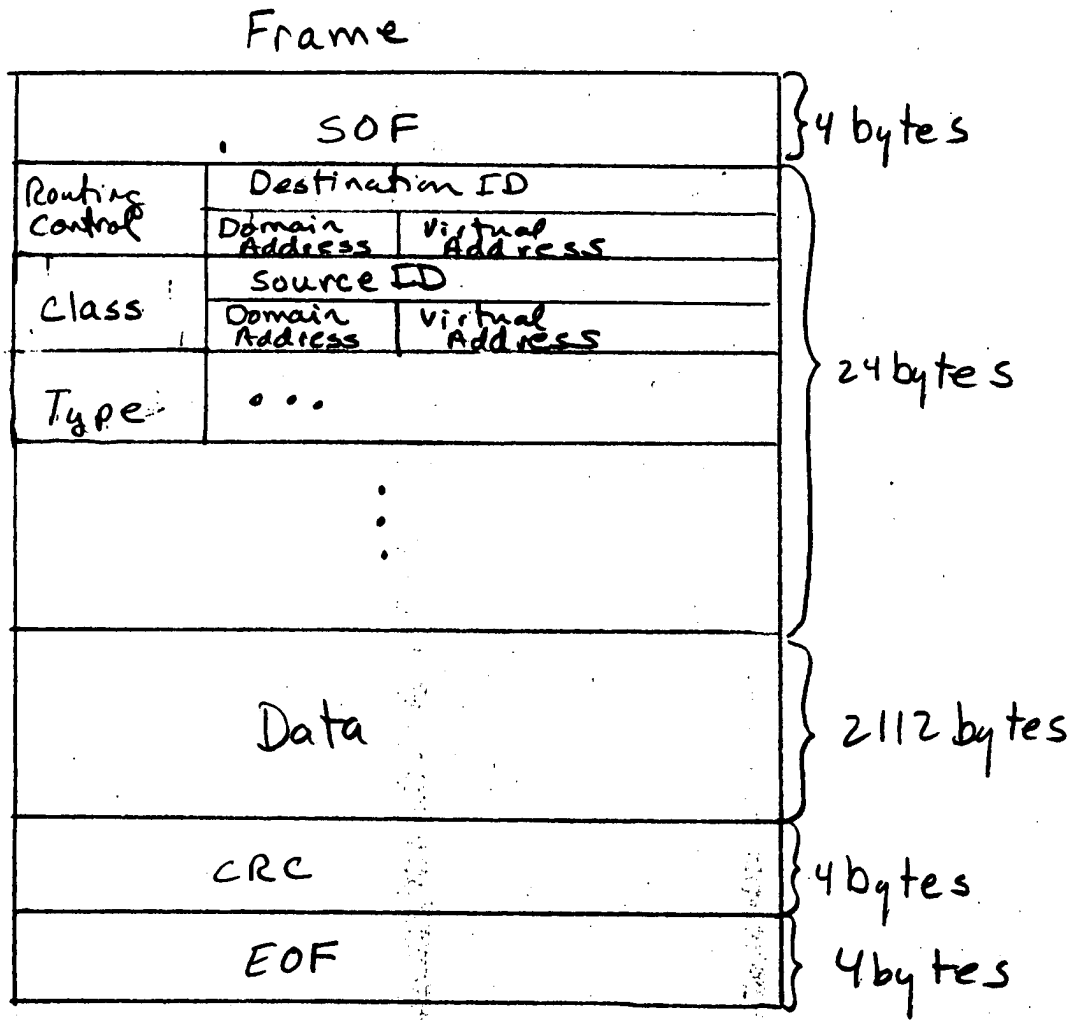


Fig 4

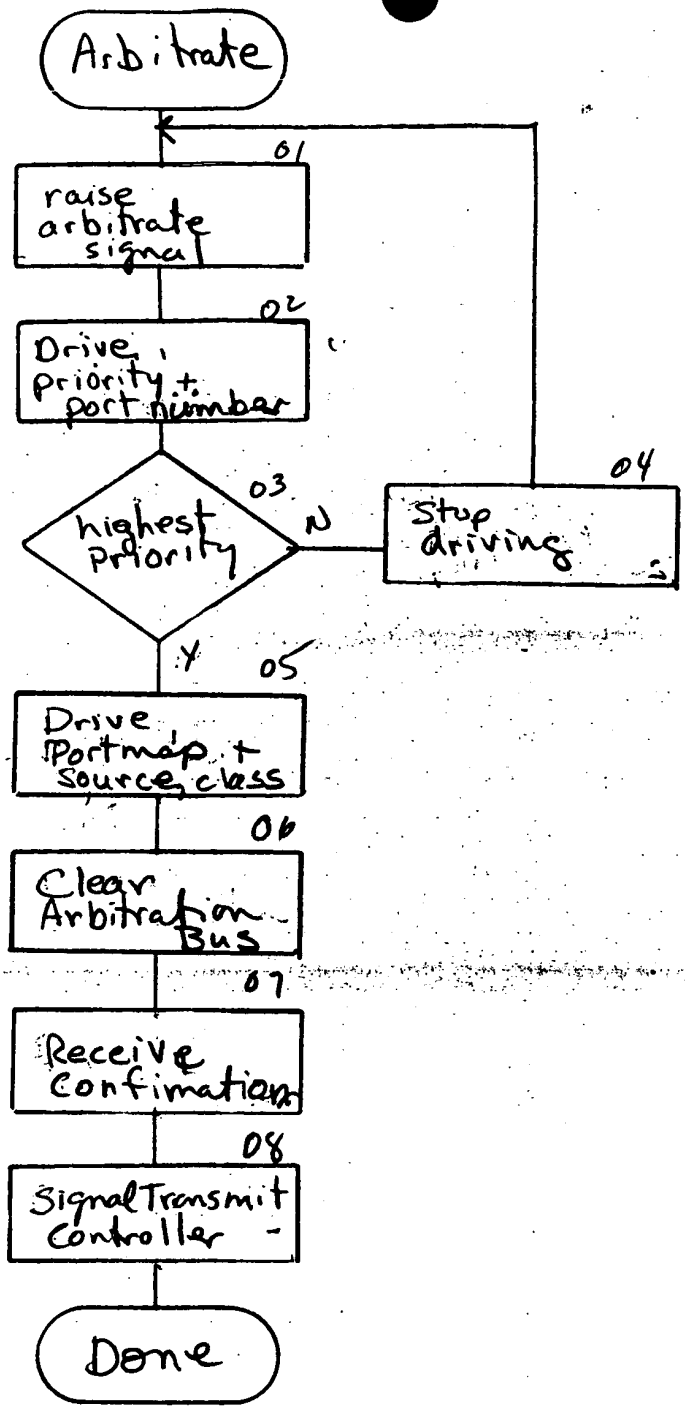


Fig 5

EP 0 457 528 A2

T0920T" 2259h00T

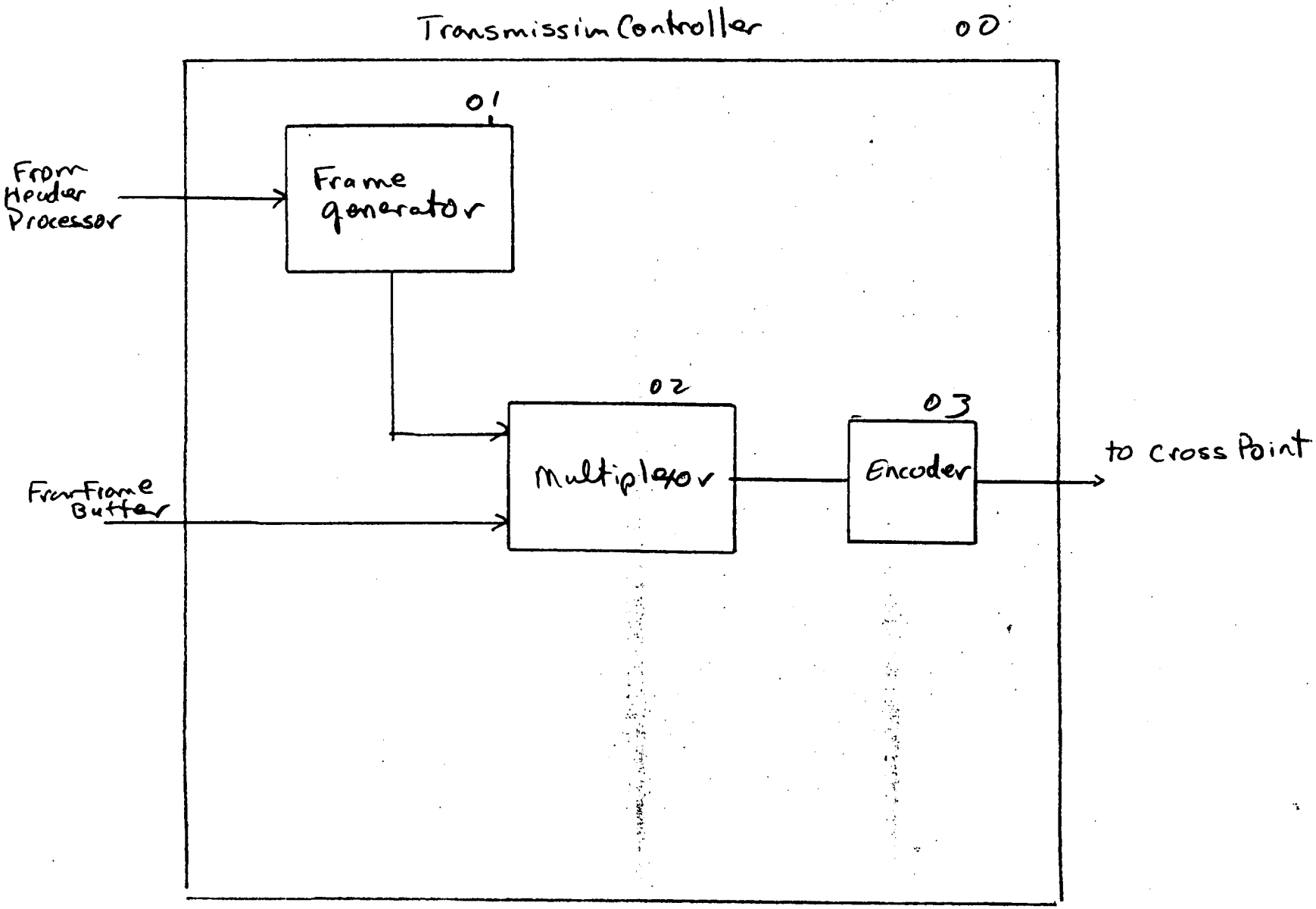


Fig 6

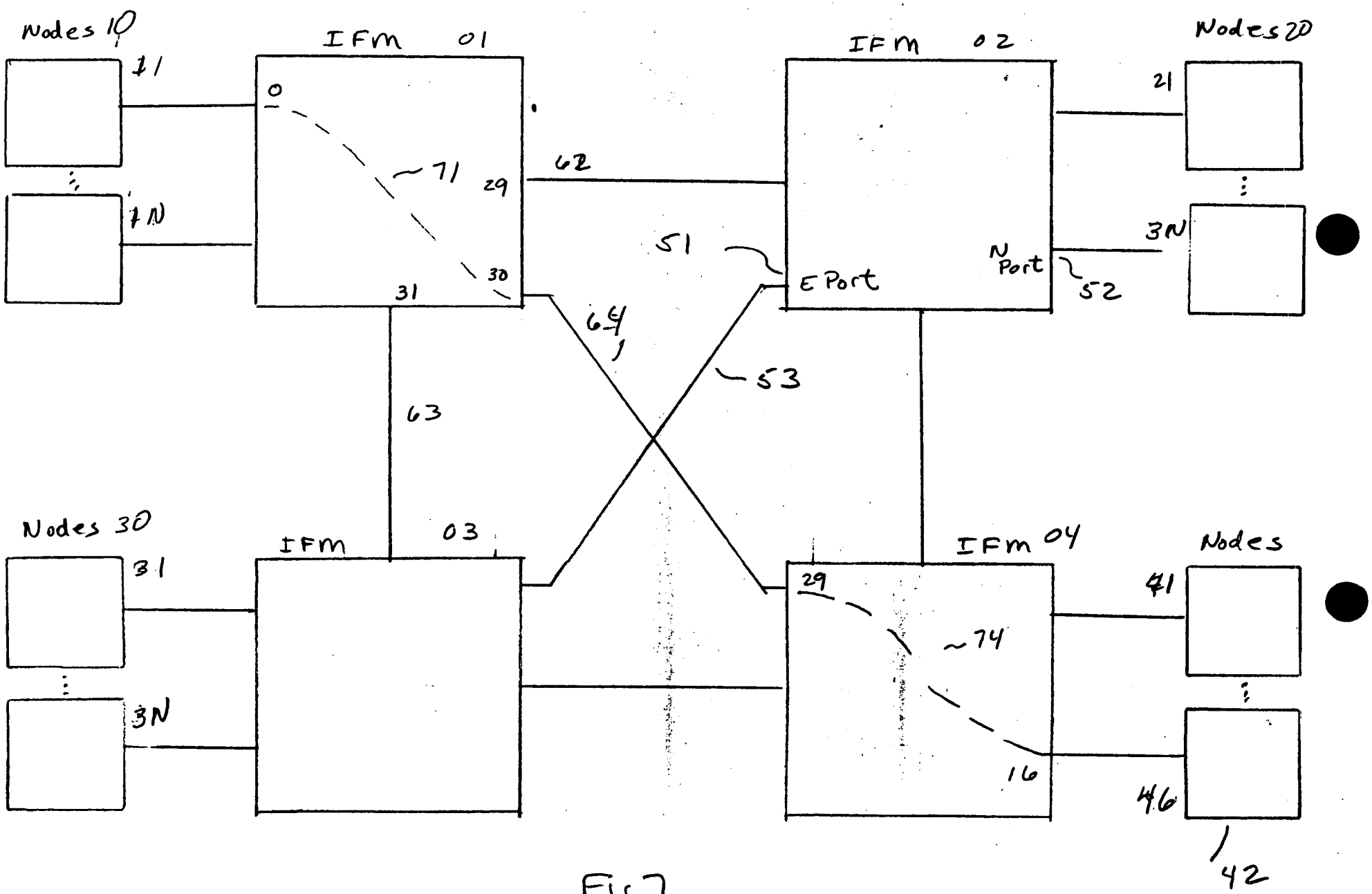

Nodes 10
11
1N
IFM 01
0
71
29
30
31
62
64
63
53
51
EPort
IFM 02
NPort
52
Nodes 20
21
3N
Nodes 30
31
3N
IFM 03
IFM 04
29
74
16
Nodes
41
46
42

Fig 7

T0920T*2594A01

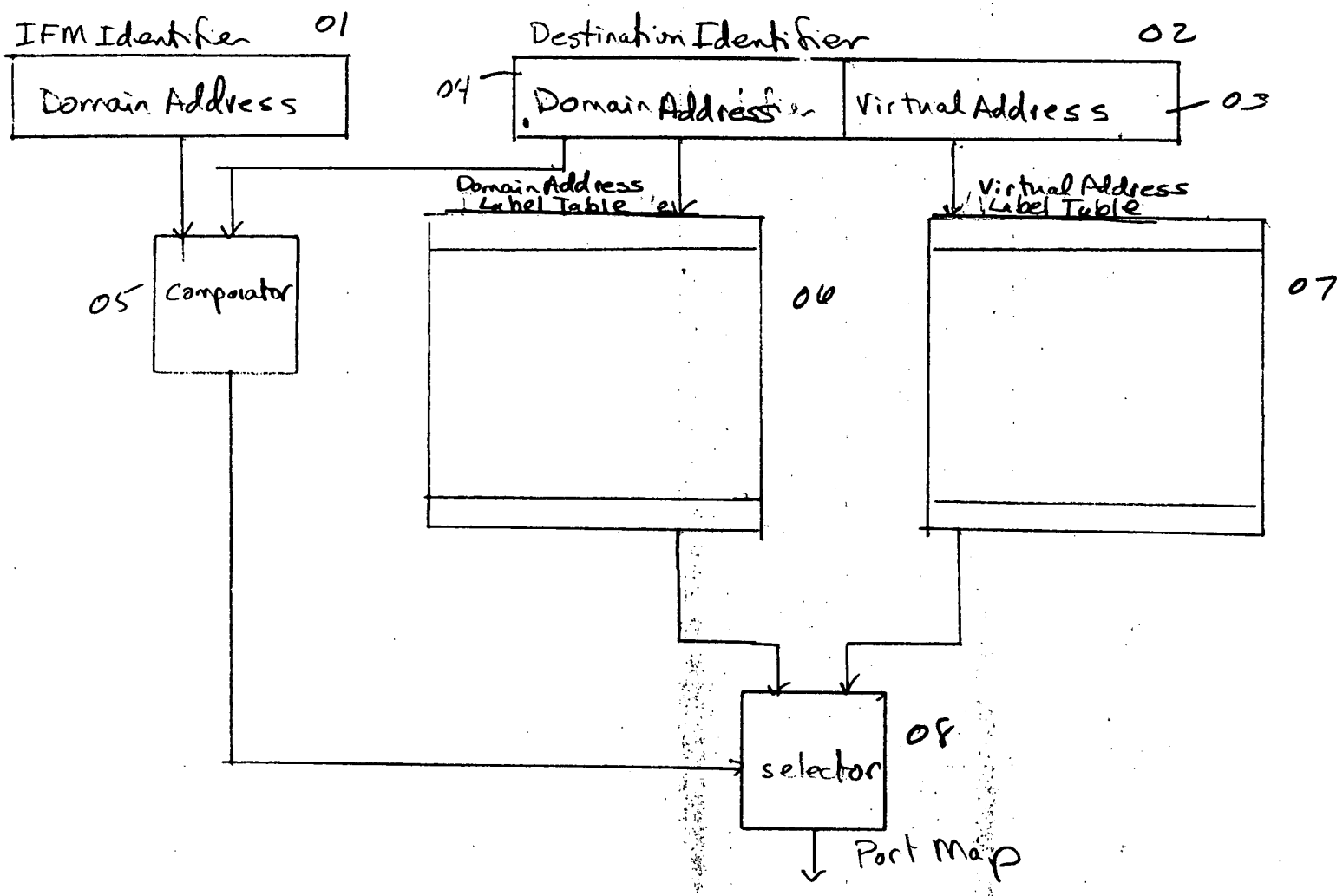


Fig. 8

T0920T*2259+00T

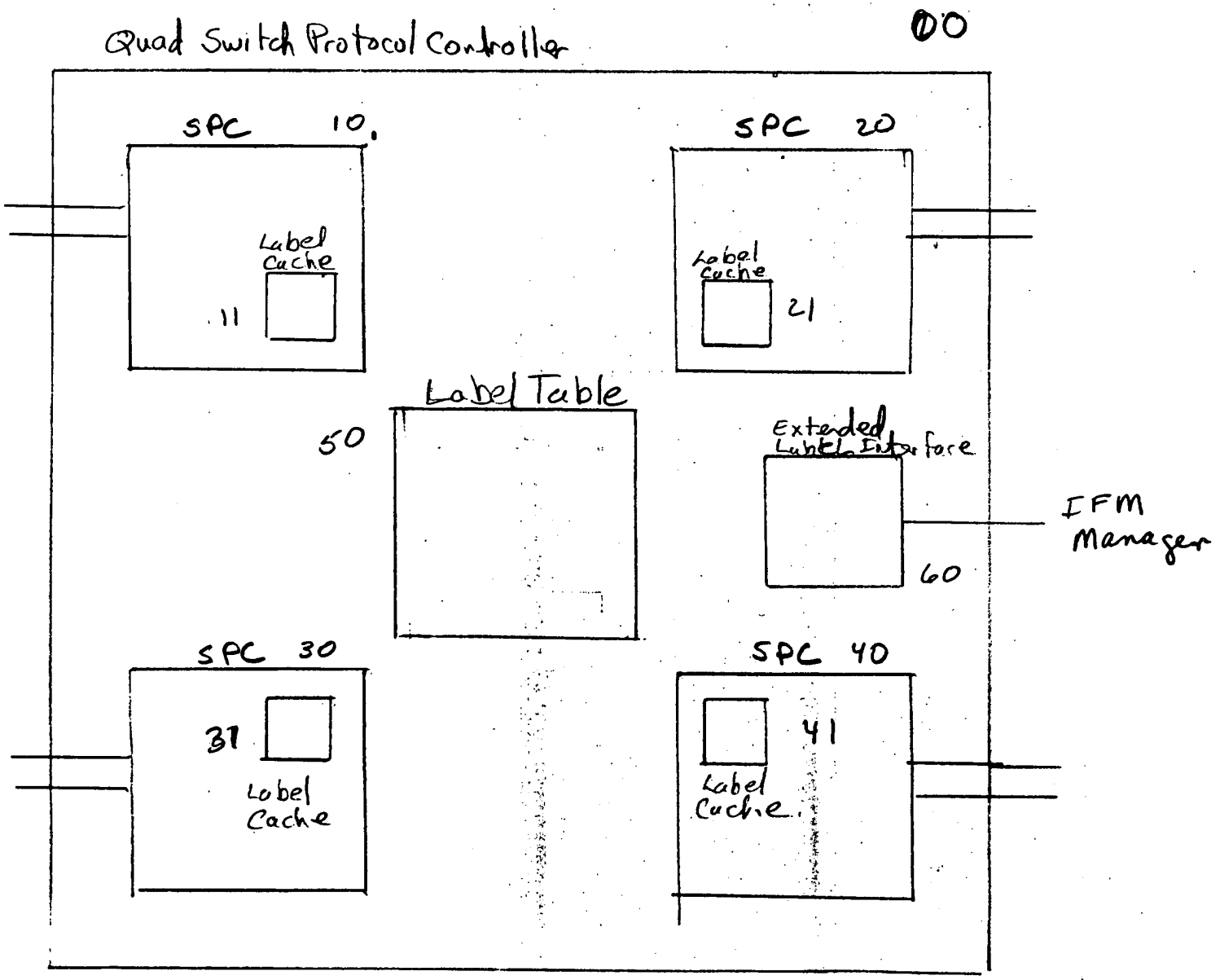


Fig 9

T09207"2759+00T

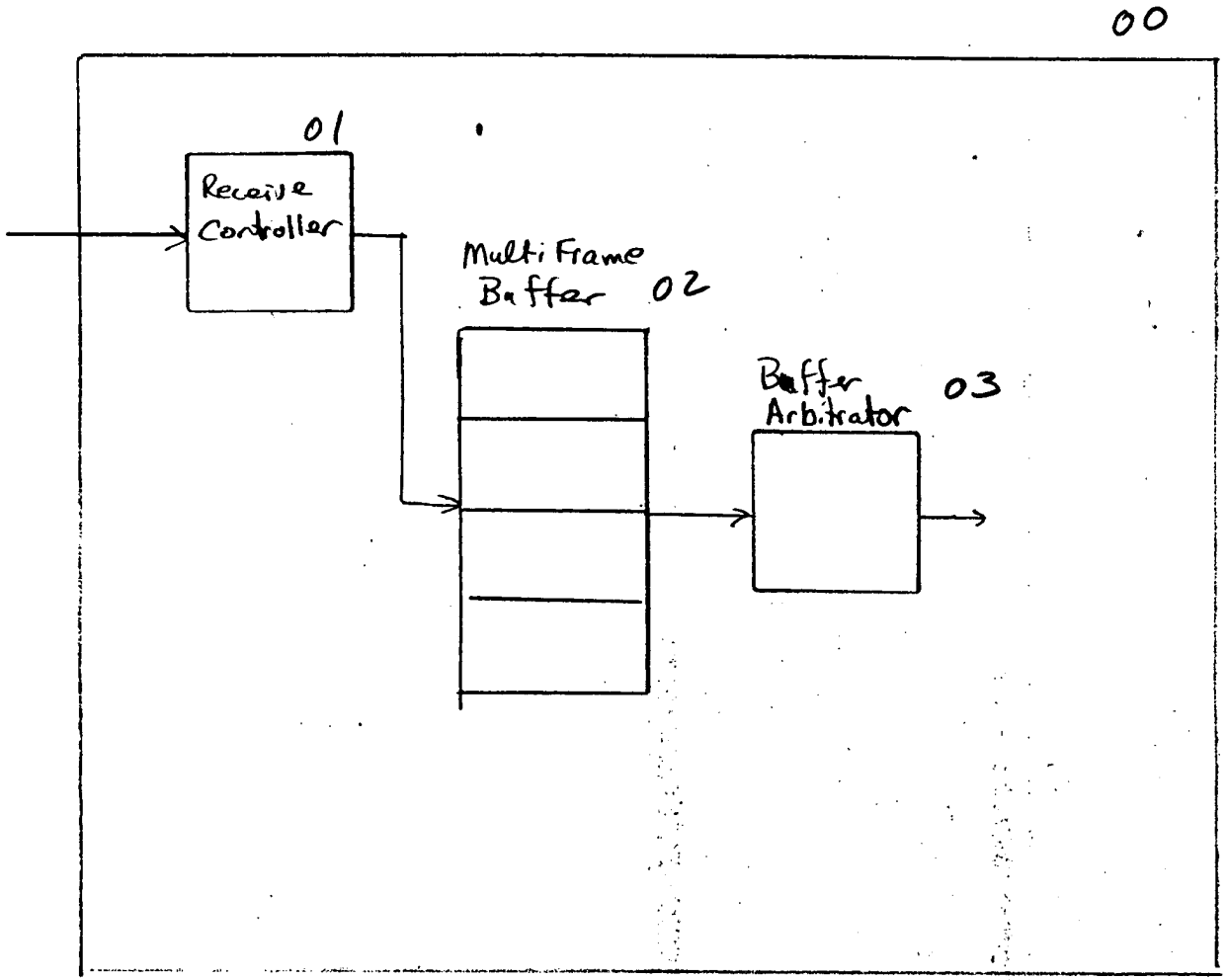


Fig 10

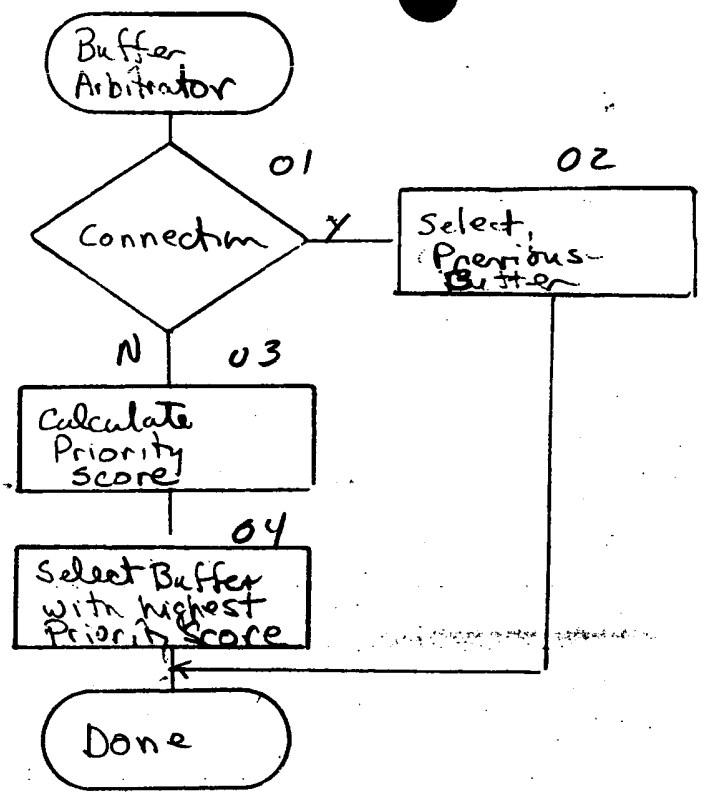


Fig 11

0045572-020203

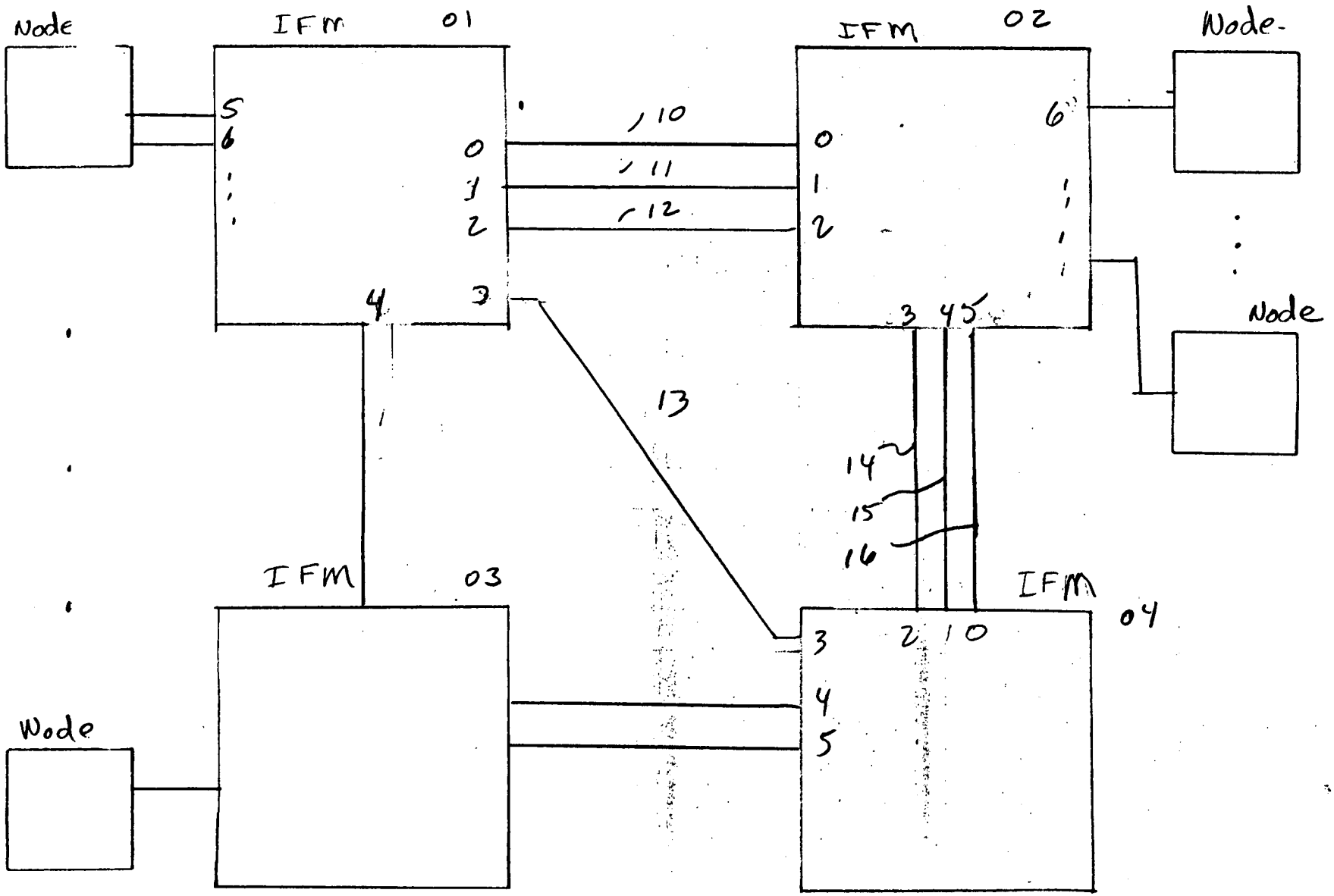

Node
IFM 01
IFM 02
Node
Node
IFM 03
IFM 04
Node
5
6
0
1
2
3
4
10
11
12
13
14
15
16

Fig 12

T0920T*2759h00T

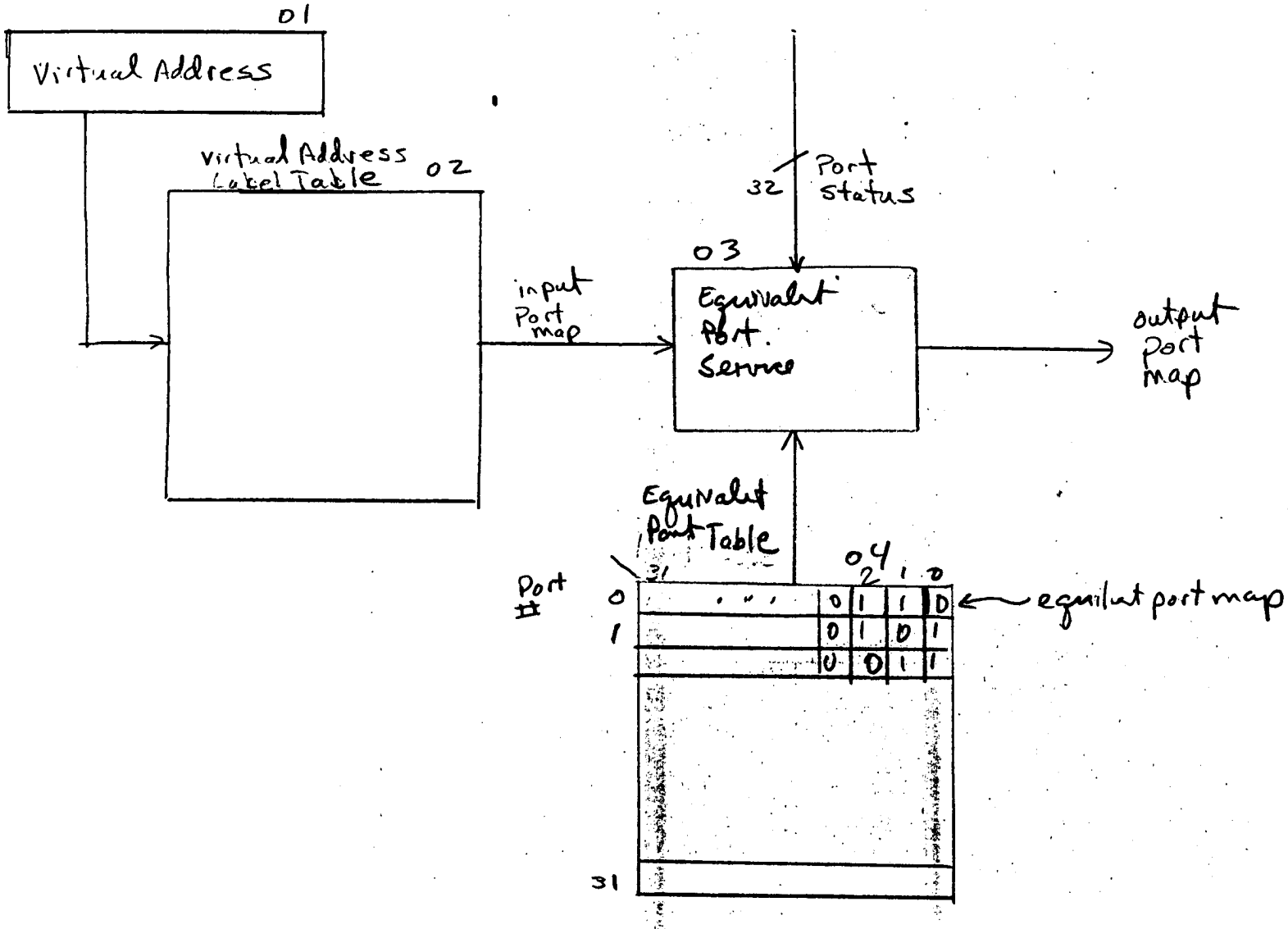


Fig 13

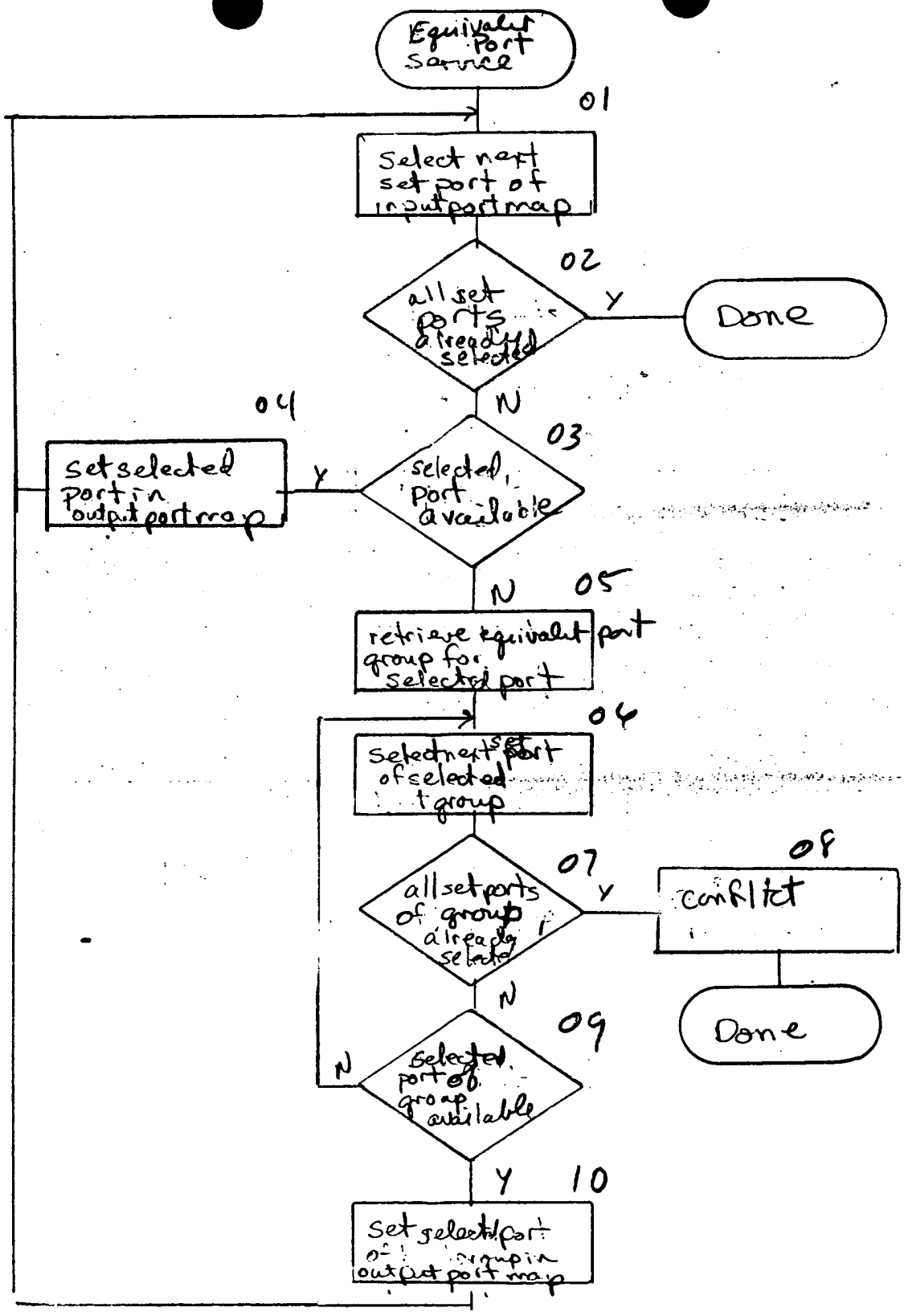

Equivalut Port Service
01
Select next set port of inputportmap
02
all set ports already selected
Y
Done
N
03
selected port available
04
set selected port in output port map
Y
N
05
retriave equivalut port group for selected port
06
Select next set port of selected t group
07
all set ports of group already selected
Y
08
conflict
Done
N
09
Selected port of group available
N
Y
10
Set selected port of group in output port map

Fig 14

T0920T* 2759h00T

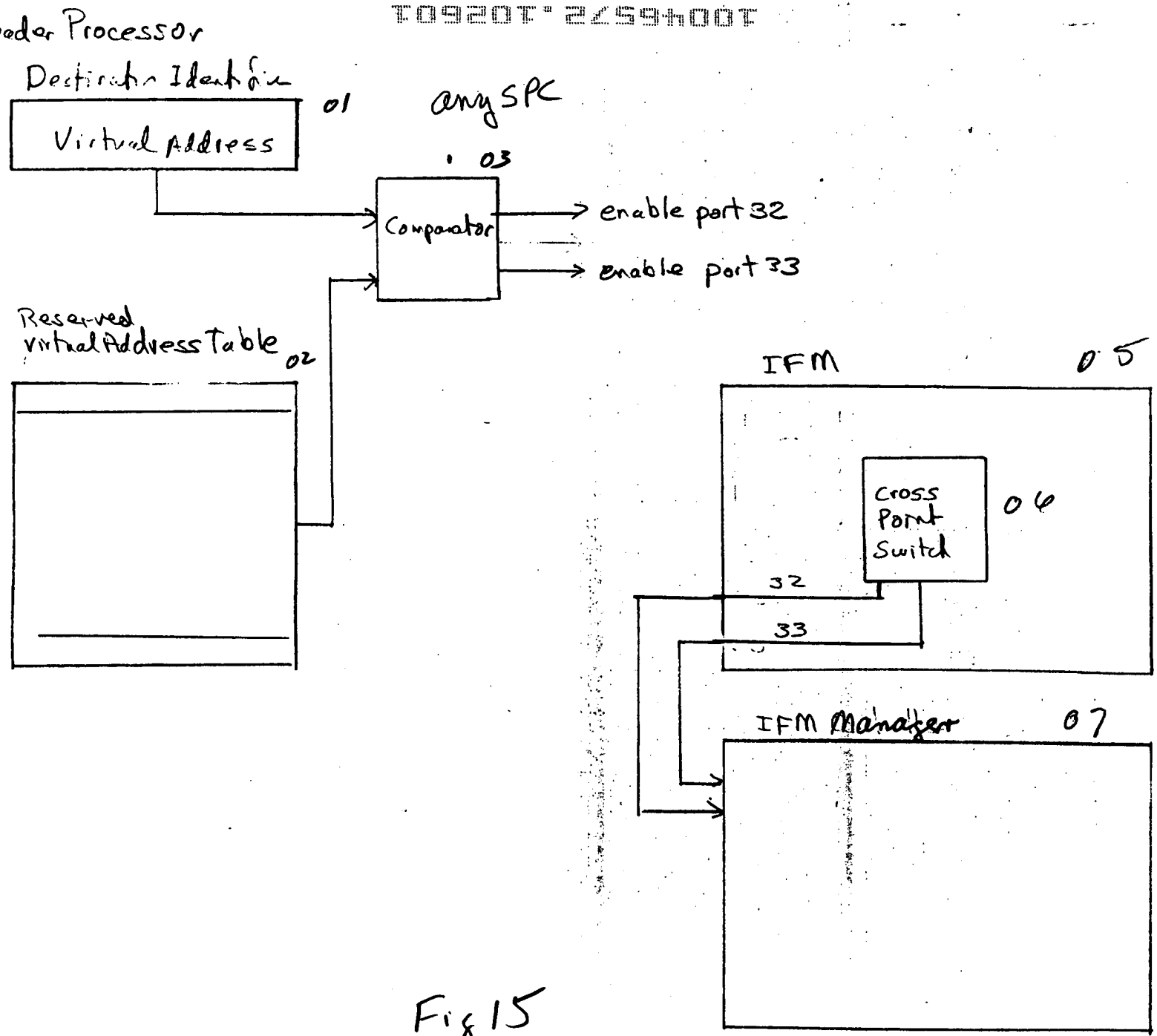
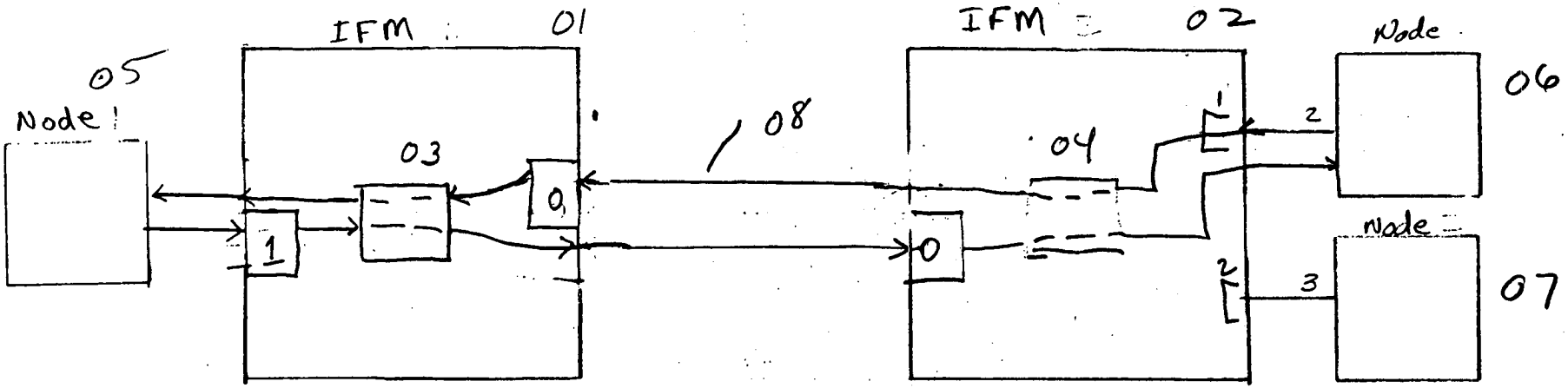


Fig 15

T09207 2594001



Deadlock

10

Time	Node 1605 IFM 1601	Node 1606 IFM 1602
0	Send start connect	Send start connect
1	Connect 1 ↔ 0	Connect 2 ↔ 0
2	Forward start connect	Forward start connect
3	Can't forward start connect Node 1	Can't forward start connect

Fig 16

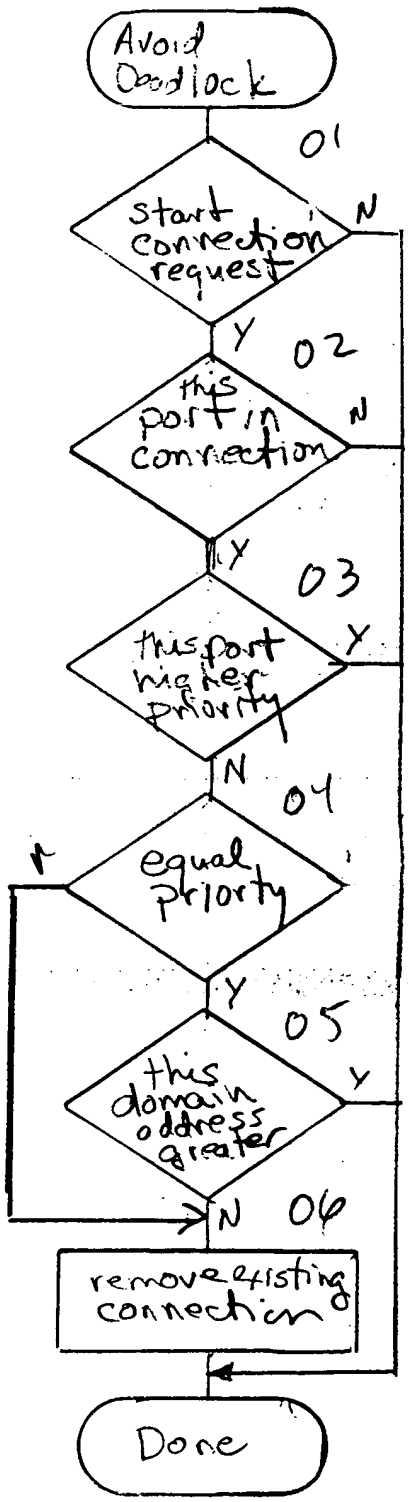


Fig 17

EP 0 465 572 B1

T09207"2594001

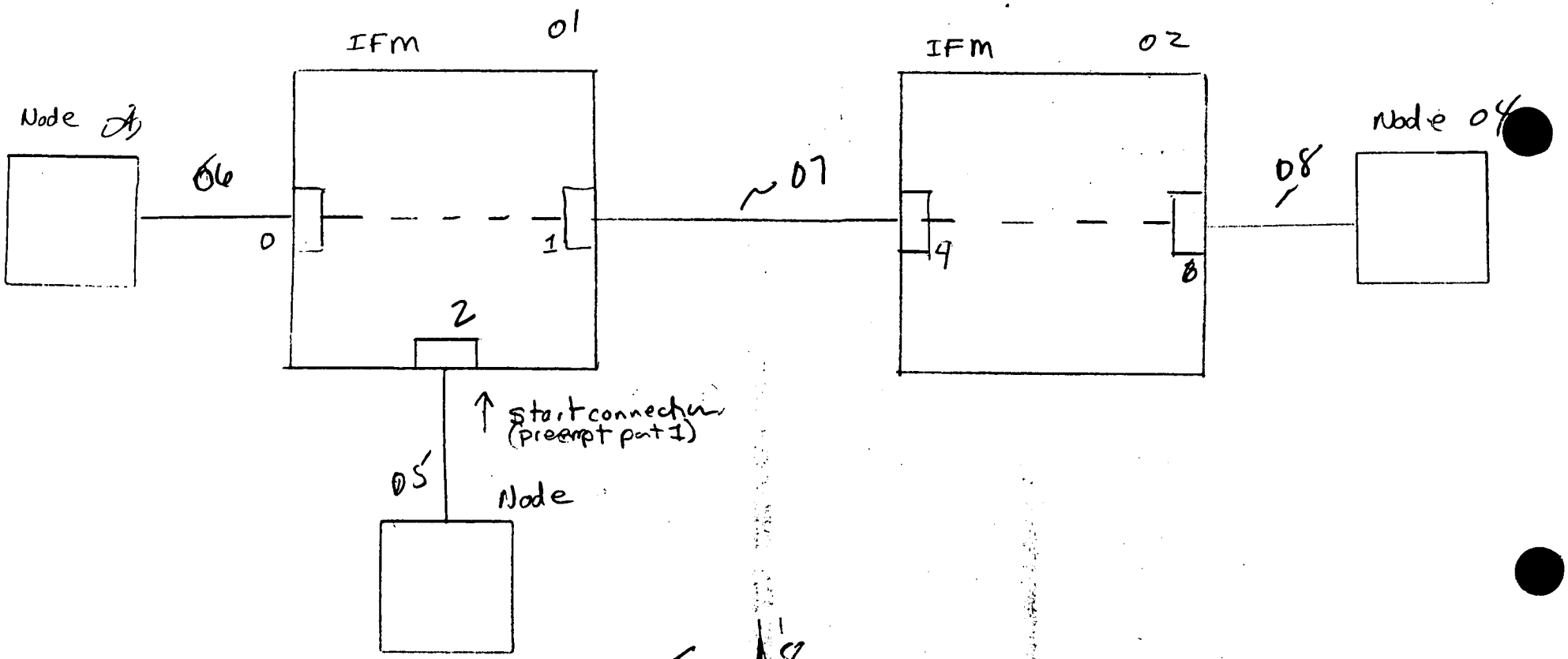


Fig 18

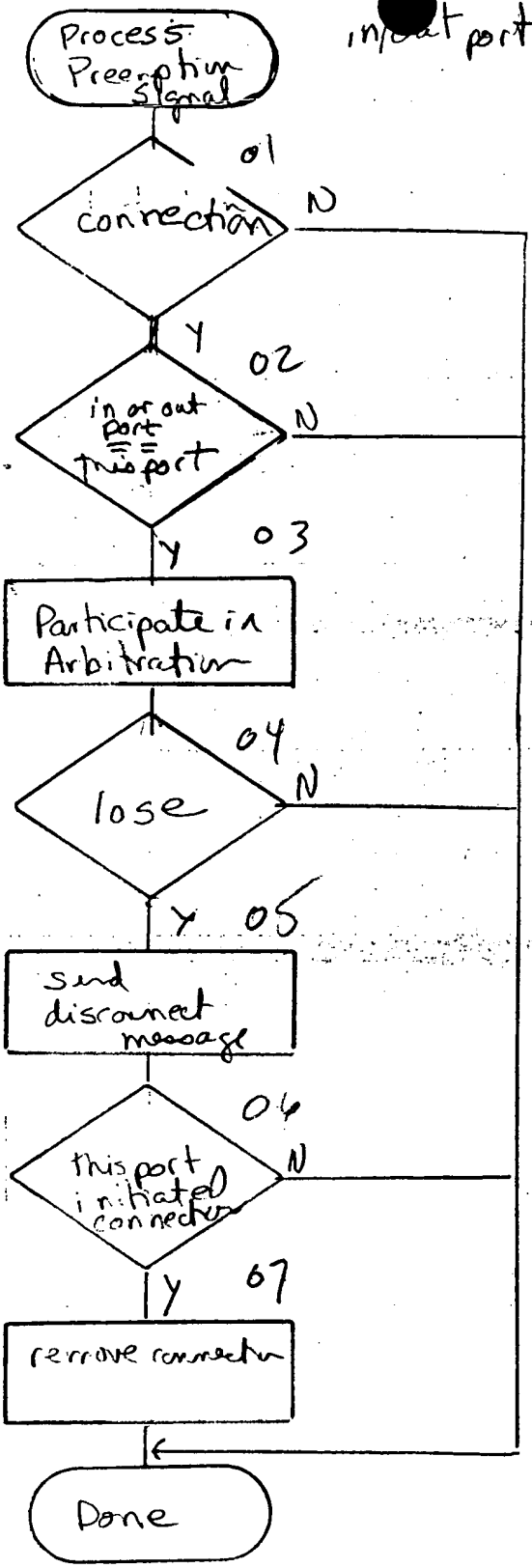


Fig. 19

10045572 - 102501

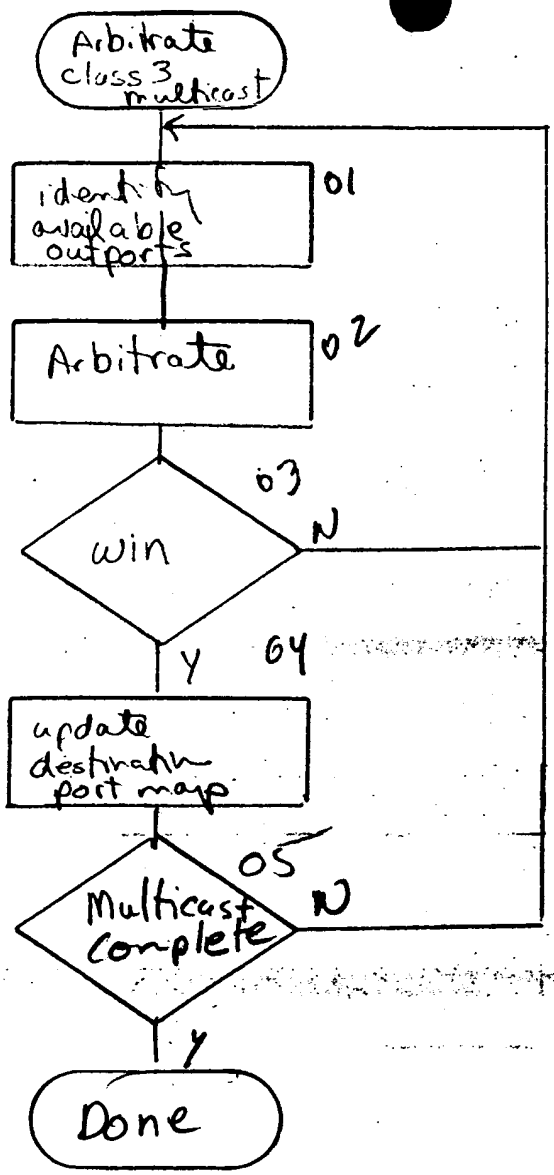


Figure 20

FOG9502 - FOG9504